

DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that: My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO. SUCH AS ADDITIONAL REPAIRS, the specification of which (check one):

is attached hereto.		
was filed on August 31	1, 2000 as United States application serial no. 09/653	3,272 and
was amended on		
was filed on	as PCT international application no.	and
was amended under Po	CT Article 19 on	
•		ified
•		
ŀ	was filed on August 3 was amended on was filed on was amended under Poreby state that I have revien, including the claims, as	was filed on August 31, 2000 as United States application serial no. 09/653

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

Prior foreign/PCT application(s):

"materiality" is defined in Title 37, Code of Federal Regulations § 1.56.

		·	Priority	Claimed
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to

(continuation page)

Invention Title: METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS ADDITIONAL REPAIRS

patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of such prior application and the national or PCT international filing date of this application:

April 15, 1999	patented
(filing date)	(status-pending, patented or abandoned)
June 6, 1997	patented
(filing date)	(status-pending, patented or abandoned)
	June 6, 1997

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

(filing date)

David V. Trask, Reg. No. 22,012 Laurence B. Bond, Reg. No. 30,549 James R. Duzan, Reg. No. 28,393 Allen C. Turner, Reg. No. 33,041 Kent S. Burningham, Reg. No. 30,453 Kevin K. Johanson, Reg. No. 38,506 Bretton L. Crockett, Reg. No. 44,632 Bradley B. Jensen, Reg. No. 46,801 Greg T. Warder, Reg. No. 50,208 Marcus S. Simon, Reg. No. 50,258 G. Scott Dorland, Ph.D., Reg. No. 51,622 Michael L. Lynch, Reg. No. 30,871

(provisional application no.)

William S. Britt, Reg. No. 20,969
Joseph A. Walkowski, Reg. No. 28,765
H. Dickson Burton, Reg. No. 48,396
Edgar R. Cataxinos, Reg. No. 39,931
Brick G. Power, Reg. No. 38,581
Krista Weber Powell, Reg. No. 47,867
Tawni L. Wilhelm, Reg. No. 47,456
Andrew F. Nilles, Reg. No. 47,825
Katherine A. Hamer, Reg. No. 47,628
Trent N. Butcher, Reg. No. 51,518
Jeffery M. Michelsen, Reg. No. 50,978
Charles B. Brantley II, Reg. No. 38,086

Address all correspondence to: <u>James R. Duzan</u>, telephone no. (801) 532-1922.

TRASKBRITT, PC P.O. Box 2550 Salt Lake City, Utah 84110

(continuation page)

Invention Title: METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS ADDITIONAL REPAIRS

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Salman Akram	
Inventor's signature	Date
Residence: Boise, Idaho	
Citizenship: Pakistan	
Post Office Address: 1463 E. Regatta St., Boise, Idaho 83	706
Full name of second joint inventor: Warren M. Farnworth	
Inventor's signature	Date
Residence: Nampa, Idaho	
Citizenship: U.S.A.	
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Full name of third joint inventor: Derek J. Gochnour	
Inventor's signature	Date
Residence: Boise, Idaho	
Citizenship: U.S.A.	
Post Office Address: 4823 S. Lippizan Avenue, Boise, Ida	tho 83709
Full name of fourth joint inventor: David R. Hembree	
Inventor's signature	Date
Residence: Boise, Idaho	
Citizenship: U.S.A.	
Post Office Address: 11074 W. Edgehill Dr., Boise, Idaho	83709

(continuation page)

Invention Title: METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS ADDITIONAL REPAIRS

Full name of fifth joint inventor: Michael E. Hess			
Inventor's signature	Date		
Residence: Kuna, Idaho			
Citizenship: U.S.A.			
Post Office Address: 19137 So. Pleasant Valley, Kuna, Ida	aho 83634		
Full name of sixth joint inventor: John O. Jacobson Inventor's signature Residence: Boise, Idaho Citizenship: U.S.A. Post Office Address: 2149 Toluka Way, Boise, Idaho 83712			
Full name of seventh joint inventor: James M. Wark			
Inventor's signature	Date		
Residence: Boise, Idaho			
Citizenship: U.S.A.			
Post Office Address: 5718 W. Drawbridge Drive, Boise, Idaho 83703			
Full name of seventh joint inventor: Alan G. Wood			
Inventor's signature	Date		
Residence: Boise, Idaho			
Citizenship: U.S.A.			
Post Office Address: 1366 E. Versailles Ct., Boise, Idaho 8	33706		



DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

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My residence, post office address and citizenship are as stated next to my name.

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I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS ADDITIONAL REPAIRS, the specification of which (check one):

UNDERGONE,	MANUFACTURING PROCEDUM , SUCH AS REPAIRS, TO SELECTIONAL REPAIRS, the specific	CT PROCEDURES T	HE IC'S W	` '	ЭO,
⊠ v v □ v	s attached hereto. was filed on August 31, 2000 as Univas amended on as PC was filed on as PC was amended under PCT Article 19	 CT international applic			ıd
-	state that I have reviewed and und cluding the claims, as amended by				
known to me to	vledge the duty to disclose to the Ube material to the patentability of defined in Title 37, Code of Feder	the subject matter clai			
365(b) of any for international applisted below and attached continuinternational applications.	claim foreign priority benefits und oreign application(s) for patent or in plication(s) designating at least on I on any attached continuation pagnation page any foreign application plication(s) designating at least on late before that of the application(s)	nventor's certificate of e country other than the e and have also identife to for patent or inventor e country other than the	r § 365(a) are United Some United Some Some Some Some Some Some Some Some	of any PCT States of Ameri and on any ate or any PCT	ca
Prior foreign/PC	CT application(s):		Priority	Claimed	
(number)	(country)	(day/month/year filed)	Yes	No	
(number)	(country)	(day/month/year filed)	Yes	No	
T. 1	11 1 1 0 1 0 1 00 1		•• •		

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patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of such prior application and the national or PCT international filing date of this application:

09/292,655	April 15, 1999	patented
(application serial no.)	(filing date)	(status-pending, patented or abandoned)
08/871,015	June 6, 1997	patented
(application serial no.)	(filing date)	(status-pending, patented or abandoned)
provisional application(s) listed be	low:	es Code, § 119(e) of any United States
(provisional application no.)	(filing date)	

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Kevin K. Johanson, Reg. No. 38,506
Bretton L. Crockett, Reg. No. 44,632
Bradley B. Jensen, Reg. No. 46,801
Greg T. Warder, Reg. No. 50,208
Marcus S. Simon, Reg. No. 50,258
G. Scott Dorland, Ph.D., Reg. No. 51,622
Michael L. Lynch, Reg. No. 30,871

William S. Britt, Reg. No. 20,969
Joseph A. Walkowski, Reg. No. 28,765
H. Dickson Burton, Reg. No. 48,396
Edgar R. Cataxinos, Reg. No. 39,931
Brick G. Power, Reg. No. 38,581
Krista Weber Powell, Reg. No. 47,867
Tawni L. Wilhelm, Reg. No. 47,456
Andrew F. Nilles, Reg. No. 47,825
Katherine A. Hamer, Reg. No. 47,628
Trent N. Butcher, Reg. No. 51,518
Jeffery M. Michelsen, Reg. No. 50,978
Charles B. Brantley II, Reg. No. 38,086

Address all correspondence to: James R. Duzan, telephone no. (801) 532-1922.

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Full name of first joint inventor: Salman Akram
Inventor's signature $\int_{a}^{b} \int_{a}^{b} \int_{$
Residence: Boise, Idaho
Citizenship: Pakistan USA
•
Post Office Address: 1463 E. Regatta St., Boise, Idaho 83706
Full name of second joint inventor: Warren M. Farnworth
Inventor's signature Wann II Date 1-29-04
Residence: Nampa, Idaho
Citizenship: U.S.A.
Post Office Address: 2004 S. Banner, Nampa, Idaho 83686-7271
2 ob Cine i ladico Di Bi Ballici, i valipa, ladico ob coo i 2 i 1
Full name of third joint inventor: Derek J. Gochnour
Tun name of simulations involved. Botok s. spormous
Inventor's signature Out Jorhum Date 1/8/04
Inventor's signature Jorhum Date 1/8/04 Residence: Boise, Idaho
Citizenship: U.S.A.
•
Post Office Address: 4823 S. Lippizan Avenue, Boise, Idaho 83709
Full name of fourth joint inventor: David R. Hembree
Inventor's signature Akual M. Mehrander Date 1-29-09
Inventor's signature Allun 1. June Date 1-27-07
Residence: Boise, Idaho
Citizenship: U.S.A.
Post Office Address: 11074 W. Edgehill Dr., Boise, Idaho 83709

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Full name of fifth joint inventor: Michael E. Hess	
Inventor's signature Mil E pro-	Date 02/19/04
Residence: Kuna, Idaho	•
Citizenship: U.S.A.	•
Post Office Address: 19137 So. Pleasant Valley, Kuna, Ida	aho 83634
Full name of sixth joint inventor: John O. Jacobson	
Inventor's signature	Date
Residence: Boise, Idaho	
Citizenship: U.S.A.	
Post Office Address: 2149 Toluka Way, Boise, Idaho 837	12
Full name of seventh joint inventor: James M. Wark	, /
Inventor's signature Residence: Boise, Idaho	Date 1/29/04
Residence: Boise, Idaho	
Citizenship: U.S.A.	
Post Office Address: 5718 W. Drawbridge Drive, Boise, I	daho 83703
Full name of seventh joint inventor: Alan G. Wood	
Inventor's signature	Date //27/04
Residence: Boise, Idaho	
Citizenship: U.S.A.	
Post Office Address: 1366 E. Versailles Ct., Boise, Idaho	83706